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# Any－polar resistive switching behavior in Ti－intercalated $\mathbf{P t} / \mathbf{T i} / \mathrm{HfO}_{2} / \mathbf{T i} / \mathbf{P t}$ device＊ 

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The special any－polar resistive switching mode includes the coexistence and stable conversion between the unipolar and the bipolar resistive switching mode under the same compliance current．In the present work，the any－polar resistive switching mode is demonstrated when thin Ti intercalations are introduced into both sides of $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ RRAM device． The role of the Ti intercalations contributes to the fulfillment of the any－polar resistive switching working mechanism， which lies in the filament constructed by the oxygen vacancies and the effective storage of the oxygen ion at both sides of the electrode interface．

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## 1．Introduction

Resistive random access memory（RRAM）devices of－ fer significant application potential in future non－volatile data storage technology ${ }^{[1-4]}$ due to their fast operation speed，high storage density，and process compatibility with today＇s sili－ con technology．${ }^{[5-7]}$ In previous research of RRAM，the resis－ tive switching modes are generally classified as unipolar re－ sistive switching（URS）mode and bipolar resistive switching （BRS）${ }^{[8]}$ mode．If the URS can symmetrically occur at both positive and negative voltages，it is also referred to as non－ polar resistive switching mode．${ }^{[9,10]}$ In recent years，coexis－ tence of URS and BRS was also studied．${ }^{[11-17]}$ According to our previous study on $\mathrm{Pt} / \mathrm{LATP} / \mathrm{Pt}$ devices，the stable conver－ sion between the unipolar mode and the bipolar mode is named any－polar resistive switching mode．${ }^{[18]}$ The crystalline chan－ nel structure of the LATP $\left(\mathrm{Li}_{1-x} \mathrm{Al}_{x} \mathrm{Ti}_{2-x}\left(\mathrm{PO}_{4}\right)_{3}\right)^{[19-21]}$ bene－ fits the easy movement and effective storage of the oxygen ion when used as a resistive switching layer，and thus contributing to the any－polar resistive switching mode．

Titanium（Ti），as a metal with high affinity with oxy－ gen，is naturally regarded as an ideal oxygen storage ma－ terial．Using Ti as an electrode or additional interfacial layer to improve the performance of RRAM device has been extensively studied．${ }^{[22,23]}$ For the widely studied $\mathrm{HfO}_{2}$ RRAM，with a thin Ti layer serving as a reactive buffer layer， the $\mathrm{TiN} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{TiN}$ device demonstrated excellent memory performance and satisfactory switching endurance over $10^{6}$ cycles．${ }^{[23]}$

In this work，the effect of Ti intercalation on the conven－ tional $\mathrm{HfO}_{2}$ resistive switching device is restudied．The Ti intercalation can be at one side of the $\mathrm{HfO}_{2}$ film，or be at each side of the $\mathrm{HfO}_{2}$ film．With various device structures，different resistive switching modes are found．When the Ti intercalation layers are inserted at both sides of the $\mathrm{HfO}_{2}$ film，the resulting $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device shows the stable any－polar resistive switching behavior．This phenomenon gives a new insight into the fundamental working mechanism of the any－polar resistive switching mode．

## 2．Results and discussion

To fabricate $\mathrm{HfO}_{2}$ RRAMs，the Pt －substrate is used as a bottom electrode．The Pt－substrate contains a structure of $\operatorname{Pt}(100 \mathrm{~nm}) / \mathrm{Ti}(50 \mathrm{~nm}) / \mathrm{Si}(100)$ ，which is prepared by succes－ sive direct current（DC）magnetron sputtering of Ti and Pt on a $\mathrm{Si}(100)$ substrate．The resistive $\mathrm{HfO}_{2}$ layer is then grown by atomic layer deposition（ALD）at $275^{\circ} \mathrm{C}$ with using tetrakis （sthylmethylamido）hafnium（TEMAH）and $\mathrm{H}_{2} \mathrm{O}$ as precur－ sors．A 5－nm－thick Ti layer is sputtered at the front side and／or at the back side of the $\mathrm{HfO}_{2}$ film as an intercalation layer． The Pt top electrodes with a diameter of $800 \mu \mathrm{~m}$ are sputtered through a mechanical mask．

As shown in Fig．1（a），three types of resistive mem－ ory devices are prepared in the present investigation，which are $\mathrm{Pt} / \mathrm{HfO}_{2}(10 \mathrm{~nm}) / \mathrm{Pt}, \mathrm{Pt} / \mathrm{HfO}_{2}(10 \mathrm{~nm}) / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ ，and $\mathrm{Pt} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{HfO}_{2}(10 \mathrm{~nm}) / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ ．For basic DC $I-V$ measurements，an external bias is applied to the top Pt elec－

[^0]trode, and the bottom Pt electrode is grounded by a Keithley 4200 semiconductor parameter analyzer. All measurements are performed respectively at room temperature, in ambient condition, and in a dark chamber. Transmission electron microscope (TEM) is used to image the cross-section of the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$-substrate device as shown in Fig. 1(b). The TEM samples are prepared by applying the in situ focused ion beam lift-out technique to a dual beam, for focused ion beam/scanning electron microscopy (FEI Company, UK).


Fig. 1. (a) Schematic diagram of RRAM devices with Ti intercalation layers, and (b) cross-section of the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device.

The simplest RRAM can be finished by a simple sandwich structure of metal/oxide/metal such as $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$. The $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ RRAM works in URS switching mode. However, the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ RRAM suffers poor endurance property. After limited switching cycles, the device tends to be break down. ${ }^{[24]}$ Figure 2(a) shows the current-voltage ( $I-V$ ) switching property of our $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ RRAM. The initial high resistance state (HRS) of the as-fabricated device is about $10^{10} \Omega$. After electroforming, the RESET voltage is around 0.5 V , and the SET voltages are in a range between 1.5 V and 4.3 V for the following cycles. The on-off ratio at a read voltage of 0.2 V is generally larger than $10^{3}$. But after 10 cycles, the device fails, ending up with final permanent low resistance state (LRS). The limited switching cycles lie in the oxygen ions migrating towards and escaping from the anode electrode during the switching. This was also confirmed by the observation of gas bubbles at the anode in early studies. ${ }^{[2]}$

To improve the switching property of the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ RRAM, titanium electrode or titanium intercalation layer is introduced. Considering that the oxygen ion is the moving species under switching operations and with the titanium layer
acting as the oxygen reservoir, the duration property of the $\mathrm{HfO}_{2}$ RRAM device is prominently improved. ${ }^{[23]}$ Once the titanium is introduced, the $\mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Pt}$ or the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ RRAM device shows bipolar switching mode. ${ }^{[25]}$ When positive bias is exerted on the Ti or the Ti intercalated electrode, the oxygen ions are driven towards and captured by Ti under the action of electric field and the high oxygen density of Ti. Oxygen vacancies accumulate in the $\mathrm{HfO}_{2}$ layer, forming a conductive filament. When the applied bias is negative, the oxygen ions are released from Ti and annihilate the filament, finishing the RESET operation.

Figure 2(b) shows the typical electrical switching property of our $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ RRAM device. The electroforming occurs at $-3.7-\mathrm{V}$ bias. A compliance current ( $I_{\mathrm{C}}$ ) of 1 mA is applied during the following SET operations. A stable bipolar resistive switching is demonstrated (negative SET, positive RESET). The device in Fig. 2(b) experiences 65 switching cycles without degradation. The RESET voltages varies around +0.5 V , and the SET voltages spread in a range from -2.4 V to -1.5 V .

The above BRS mode bring the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ into stable and robust switching behavior. But on the other hand, if we change the polarities of the BRS operation, i.e., negative RESET and positive SET, the device is corrupted soon after only a few cycles. This phenomenon can be understood by the asymmetric structure of the device, which means that the drifting oxygen ions in $\mathrm{HfO}_{2}$ can be stored only at the Ti intercalated electrode rather than the counter pure Pt electrode. Obviously, the polarity of the BRS operation in the device of $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ is unchangeable.

To implement the so-called any-polar resistive switching properties, the RRAM device needs to possess both the BRS and the URS properties simultaneously. Secondly, the polarities of both operations must be changeable. Finally, each operation mode and each polarity can be exchanged freely, which is independent of their operation history. Up to now, neither of the above two devices $\left(\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}\right.$ and $\left.\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}\right)$ can be called any-polar resistive switching device.

To implement the any-polar resistive switching mode, the key issue is the ability of the oxygen ions to be stored at both electrode sides of the resistive layer, considering that oxygen vacancy is the species to set up the filament. In the device of the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$, oxygen ions can be stored only at the bottom electrode. So, adding another Ti intercalation layer to the top Pt electrode can straightforwardly improve the storage of oxygen. This idea leads to the symmetric device structure of $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$. A TEM cross-section image of the fabricated device is shown in Fig. 1(b).

For the symmetric $\mathrm{Pt} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{HfO}_{2}(10 \mathrm{~nm}) / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ RRAM device, the resistive switching property is shown in Fig. 2(c). An electroforming process occurs at -3.725 V to
active the device. After forming, 120 uniform operation cycles are captured without any degradation. By recognizing the voltage polarity for each of the SET and the RESET process, all the switching operations can be grouped into four different resistive switching sub-modes, which are named as URS + , URS - , BRS + , and BRS- as shown in Fig. 2(d), respectively. In each conversion process, the conversion of two sub-switching modes is realized by changing the applied voltage polarity of one of the SET and the RESET process, with the voltage polarity of the other process remaining unchanged. In URS + , the SET and the RESET processes are both completed at positive voltages. For URS-, the SET and the RESET processes are both executed under negative voltages. For BRS+, the SET process is completed with negative bias, and the RESET process is completed with positive bias. For BRS, opposite polarities of the SET and the RESET process may be determined by referring to the BRS+ mode.

The test in Fig. 2(c) starts from BRS+ mode. After 5-10 BRS+ cycles, the operation changes into the URS+ mode for another 5-10 cycles. The device subsequently undergoes the BRS- and URS- modes and finally comes back to the BRS+ mode. The above transition following the sequence of BRS+, URS+, BRS-, URS- is called a big-loop cycle. The device can also well follow the opposite big-loop cycle with the sequence of BRS+, URS--, BRS--, and URS+. Whether the mode is URS+ (or URS-) or BRS+ (or BRS-), the on-off ratio is always higher than 100 , the absolute value of the SET and the RESET operation are both around 2.0 V and 0.5 V , respectively.

A comparison among the above three devices is shown in Fig. 3. The resistance distributions of both the HRS and the LRS at $\pm 0.2 \mathrm{~V}$ for the three devices are shown in Fig. 3(a).

Larger memory windows are observed for the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ device without Ti intercalation than for the other two devices. But as already known, the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ device suffers poor endurance. For the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device and the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device, both have stable distributions of the HRS and LRS resistances. The switching ratio of HRS to LRS for each device is generally larger than 100. A comparison of SET/RESET voltage distributions among the three devices is shown in Fig. 3(b). It is found that the SET/RESET voltage distributions of the bipolar $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device fall well into those of the any-polar $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device, which means that when the any-polar $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device works in its BRS-sub-mode, its behavior is similar to that of the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device.

The migration of ions driven by electric field is the main motivation for resistive switching. Joule heating is another factor to redistribute ions. According to the type of the migration ions, electrochemical metallization memory and valance change memory are sorted. In the former, the conducting filaments are comprised of reduced active metal ions such as $\mathrm{Ag}+$ or $\mathrm{Cu}+$, which may drift, forming a metal electrode. ${ }^{[26-30]}$ In the latter, the filaments are formed by oxygen vacancies. ${ }^{[6]}$ The oxygen vacancy model is applicable for all the three devices in this work.

The $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ also has a symmetric device structure. Its URS property can also be observed with negative bias (denoted as URS-). The symmetric occurrence of URS+ and URS- is also called non-polar resistive switching mode. ${ }^{[9]}$ However, further study finds that the conversion between the two URS modes is impossible. Frequently changing between the URS+ and the URS- causes the device to break down immediately.


Fig. 2. Typical $I-V$ switching characteristics based on $\mathrm{HfO}_{2} \mathrm{RRAM}$ with different Ti intercalation layers. (a) Unipolar resistive switching mode of $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$ RRAM, (b) bipolar resistive switching mode of $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt}$ RRAM, (c) any-polar resistive switching mode of $\mathrm{Pt} / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{HfO} 2 / \mathrm{Ti}(5 \mathrm{~nm}) / \mathrm{Pt} \mathrm{RRAM}$, and (d) four resistive switching sub-modes extracted from curves in panel (c).


Fig. 3. (a) Switching resistance distributions of three devices of $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Pt}$, $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ and $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ at $\pm 0.2 \mathrm{~V}$. (b) SET and RESET voltage distributions of these devices.

The working mechanism of the bipolar $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ RRAM is depicted in Fig. 4(a). The restoring and releasing of oxygen ions by the Ti intercalation prevent oxygen ions from vanishing during the switching. Thus, the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device possesses a satisfactory endurance property. When adding Ti intercalation to both sides of the $\mathrm{HfO}_{2}$ layer, both BRS+ and BRS- operations are now possible. The working mechanism of the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ RRAM is depicted in Fig. 4(b).

When studying the RESET switches of the bipolar $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device, Joule heating effect and the electric migration effect are difficult to clearly separate. Both are be-
lieved to contribute to the annihilation of the filaments. When considering the BRS+ and URS+ sub-modes in the any-polar device of $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$, it is comprehended that the migration effect may be helpful in implementing the operation of BRS+, but undoubtedly useless in the operation of UPS+. But in Fig. 2(d), the RESET curves of the BRS+ and URS+ submodes are nearly identical. Considering the similarity between the RESET curves of BRS+ and URS-, Joule heating effect rather than the migration effect should be the main mechanism during all the RESET switches for the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device, including all four sub-modes.

The any-polar resistive switching property was first proposed in the study of $\mathrm{Pt} / \mathrm{LATP} / \mathrm{Pt} .{ }^{[18]}$ The special switching property is ascribed to the unique crystalline structure of the LATP, which provides the abundant transport routes and storage sites for oxygen ions. In this study, the any-polar switching property is discovered in the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device. A comparison between both devices indicates that to fulfill the requirement for the any-polar resistive switching, the transport routes and the storage medium for oxygen ions do not have to be in the same layer. In the case of $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$, the $\mathrm{HfO}_{2}$ acts as the transport route while the Ti intercalations serve as the storage medium. In this sense, if the $\mathrm{HfO}_{2}$ layer is replaced by other conventional resistive oxides, such as $\mathrm{Ta}_{2} \mathrm{O}_{5}{ }^{[31]}$ or $\mathrm{TiO}_{2},{ }^{[32]}$ the any-polar resistance switching property can still be implemented.

Finally, the current transportation mechanisms of these types of devices are explored. Typical $I-V$ curves of the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ and $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ are re-plotted on a $\log -\log$ scale as shown in Fig. 5. Whether they are in LRS or HRS, the $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ and $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ devices have the slopes of $\log (|I|)$ versus $\log (|V|)$ that are both close to unity, indicating that the ohmic conduction ${ }^{[33]}$ is the dominant mechanism in LRS or HRS region.


Fig. 4. Working mechanism of (a) $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device and (b) $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device. Notice that most of oxygen ions are stored in Ti intercalation layer. The four sub operation modes are URS+, URS-, BRS+, and BRS--


Fig. 5. Current fitting and current transportation mechanism of resistive switching properties, with blue and red lines representing $\mathrm{Pt} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ and $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$.

## 3. Conclusions

In summary, we demonstrate any-polar resistive switching behavior in Ti-intercalated $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ device. The any-polar resistive switching comprised of four sub-modes which are BRS+, URS+, BRS-, and URS-. The filaments formed by oxygen vacancies explain the switching mechanism. During switching, the $\mathrm{HfO}_{2}$ acts as the transport route while the Ti intercalation layers serve as the storage medium. The investigation of the $\mathrm{Pt} / \mathrm{Ti} / \mathrm{HfO}_{2} / \mathrm{Ti} / \mathrm{Pt}$ RRAM presents a new insight into the fundamental working mechanism of the any-polar resistive switching device.

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